

August 1986 Revised February 2000

DM7416

Hex Inverting Buffers with High Voltage Open-Collector Outputs

General Description

This device contains six independent gates each of which performs the logic INVERT function. The open-collector outputs require external pull-up resistors for proper logical operation.

Pull-Up Resistor Equations

$$R_{MAX} = \frac{V_{O} (Min) - V_{OH}}{N_{1} (I_{OH}) + N_{2} (I_{IH})}$$

$$\mathsf{R}_{MIN} = \frac{\mathsf{V}_{O}\left(\mathsf{Max}\right) - \mathsf{V}_{OL}}{\mathsf{I}_{OL} - \mathsf{N}_{3}\left(\mathsf{I}_{IL}\right)}$$

Where:

 N_1 (I_{OH}) = total maximum output high current

for all outputs tied to pull-up resistor

 N_2 (I_{IH}) = total maximum input high current for

all inputs tied to pull-up resistor

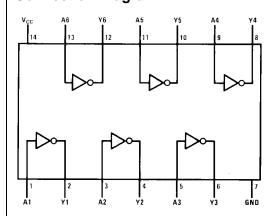
 ${\rm N_3}~({\rm I_{IL}}) = {\rm total}$ maximum input low current for

all inputs tied to pull-up resistor

Ordering Code:

Order Number	Package Number	Package Description				
DM7416	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide				

Connection Diagram



Function Table

H = HIGH Logic Level L = LOW Logic Level

Absolute Maximum Ratings(Note 1)

Supply Voltage 7V Input Voltage 5.5V Output Voltage 15V Operating Free Air Temperature Range $0^{\circ}\text{C to } +70^{\circ}\text{C}$

TV the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings.

The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Note 1: The "Absolute Maximum Ratings" are those values beyond which

Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Units
V _{CC}	Supply Voltage	4.75	5	5.25	V
V _{IH}	HIGH Level Input Voltage	2			V
V _{IL}	LOW Level Input Voltage			0.8	V
V _{OH}	HIGH Level Output Voltage			15	V
I _{OL}	LOW Level Output Current			40	mA
T _A	Free Air Operating Temperature	0		70	°C

-65°C to +150°C

Electrical Characteristics

Storage Temperature Range

over recommended operating free air temperature range (unless otherwise noted)

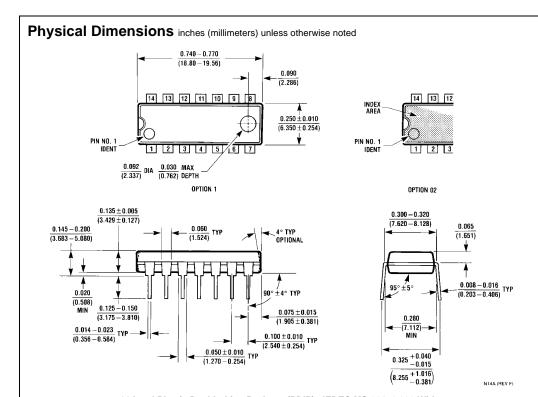
Symbol	Parameter	Conditions	Min	Typ (Note 2)	Max	Units
VI	Input Clamp Voltage	$V_{CC} = Min, I_I = -12 \text{ mA}$			-1.5	V
I _{CEX}	HIGH Level	$V_{CC} = Min, V_O = 15V$			250 μΑ	^
	Output Current	V _{IL} = Max				μΑ
V _{OL}	LOW Level	V _{CC} = Min, I _{OL} = Max			0.7	V
	Output Voltage	V _{IH} = Min			0.7	
		I _{OL} = 16 mA, V _{CC} = Min			0.4	
II	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 5.5V$			1	mA
I _{IH}	HIGH Level Input Current	$V_{CC} = Max, V_I = 2.4V$			40	μΑ
I _{IL}	LOW Level Input Current	$V_{CC} = Max, V_I = 0.4V$			-1.6	mA
I _{CCH}	Supply Current with Outputs HIGH	V _{CC} = Max		30	48	mA
I _{CCL}	Supply Current with Outputs LOW	V _{CC} = Max		27	51	mA

Note 2: All typicals are at $V_{CC} = 5V$, $T_A = 25$ °C.

Switching Characteristics

at $V_{CC} = 5V$ and $T_A = 25^{\circ}C$

Symbol	Parameter	Conditions	Min	Max	Units
t _{PLH}	Propagation Delay Time	C _L = 15 pF		15	ns
	LOW-to-HIGH Level Output	$R_L = 110\Omega$			
t _{PHL}	Propagation Delay Time]		23	
	HIGH-to-LOW Level Output		23	23	ns



14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide Package Number N14A

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